

FIG. 1

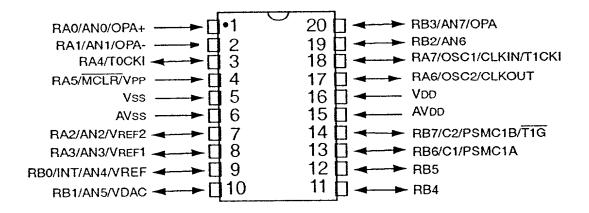
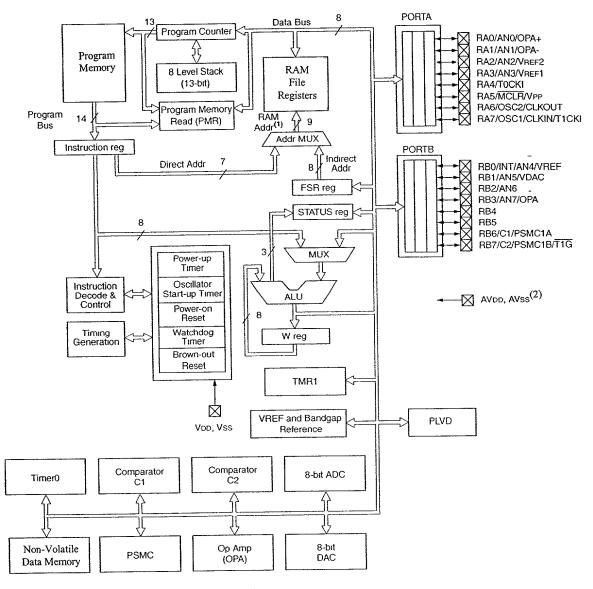


FIG. 2

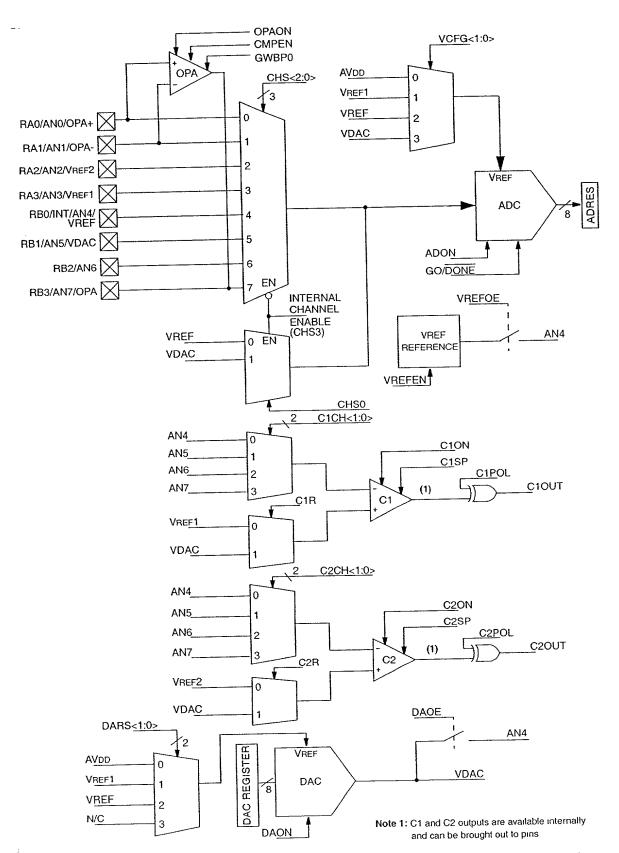
FIG. 3



Note 1: Higher order bits are from the STATUS register.

2. AVDD and AVSs pins are used for the following modules: C1, C2, OPA, DAC, ADC, and VREF.

FIG. 4



Legend: C1 = Comparator 1, C2 = Comparator 2, OPA = Operational Amplifier, VREF = Voltage Reference Low.